



Srishyala Educational Trust (R)

GM INSTITUTE OF TECHNOLOGY

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

CLASS TIME TABLE

W.E.F: 31-10-2022

Academic Year		Scheme	Semester	Section	Room No	Class Coordinator	Semester Commencement		Last Working Day	
2022-23		2021	III	A	324	Revathi S N	31-10-2022		11-2-2023	
Time	8.30 am To 9.30 am	9.30 am To 10.30 am	10.30 am To 10.50 am	10.50 am To 11.50 am	11.50 am To 12.50 pm	12.50 pm To 1.45 pm	1.45 pm To 2.40 pm	2.40 pm To 3.35 pm	3.35 pm To 4.30 pm	
Day	MON	DSDV	←----- ADE(A1)/LIC(A2) -----→			LUNCH BREAK	AEC	DSDV	Lib	
	TUE	BSP	AEC	TEA BREAK	DSDV		MAT	←----- ADE(A2)/LIC(A3) -----→		
	WED	AEC	KAN		DSDV		BSP	MAT	BSP	SCR
	THU	SEM	←----- ADE(A3)/LIC(A1) -----→					←-Mentor Mentee Interaction/Dip Maths -→		
	FRI	SEM	MAT	TEA BREAK	KAN		AEC	←----- NSS/PE/YOGA-----→		
	SAT	MAT	DSDV		BSP		BSP			
Allocation of Subjects							Faculty Mentors			
Subject Name		Subject Code		Course Coordinator		Batch	Faculty Name			
Mathematics		21MAT31		Dr. Manjula G M		A1	Santhosh B G			
Digital System Design Using Verilog		21EC32		Hanumanth Raju R K		A2	Deepa T			
Basic Signal Processing		21EC33		Dr. Kavitha K J		A3	Hanumanth Raju R K			
Analog Electronic Circuits		21EC34		Revathi S N		Internal Assessment				
Analog and Digital Electronics Lab		21ECL35		Revathi S N/Manjula B K		IA	Date			
Social Connect and Responsibility		21UH36		Santhosh BG/ Deepa T						
Samskrutika Kannada		21KSK37/21KBK37		Deepa B S						
LIC Lab Using Multisim		21EC383		Raviteja B/HGC/SKB/SGR						
NSS/PE/YOGA		21NS/PE/YO								

Sath N. S
Coordinator(s)

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Head of the Department
Head of the Department
Dept. of Electronics & Comm. Engg.
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Academic Year		Scheme	Semester	Section	Room No	Class Coordinator	Semester Commencement		Last Working Day
2022-23		2021	III	B	330	Asha T	31-10-2022		11-2-2023
Time	8.30 am To 9.30 am	9.30 am To 10.30 am	10.30 am To 10.50 am	10.50 am To 11.50 am	11.50 am To 12.50 pm	12.50 pm To 1.45 pm	1.45 pm To 2.40 pm	2.40 pm To 3.35 pm	3.35 pm To 4.30 pm
Day	MON	DSDV	AEC	DSDV	KAN	LUNCH BREAK	←----- ADE(B1)/LIC(B2) -----→		
	TUE	Seminar	←----- ADE(B2)/LIC(B3) -----→				MAT	BSP	BSP
	WED	MAT	BSP	DSDV	AEC		←----- ADE(B3)/LIC(B1) -----→		
	THU	AEC	MAT	KAN	DSDV		←-Mentor Mentee Interaction/Dip Maths -→		
	FRI	Seminar	BSP	DSDV	SCR		←----- NSS/PE/YOGA-----→		
	SAT	BSP	AEC	MAT	LIB				
			TEA BREAK						

Allocation of Subjects				Faculty Mentors	
Subject Name	Subject Code	Course Coordinator	Batch	Faculty Name	
Mathematics	21MAT31	Dr. Manjula G M	B1	Gayathri S G	
Digital System Design Using Verilog	21EC32	Asha T	B2	Revathi S N	
Basic Signal Processing	21EC33	Dr. Kavitha K J	B3	Asha T	
Analog Electronic Circuits	21EC34	Gayatri S G	Internal Assessment		
Analog and Digital Electronics Lab	21ECL35	Gayatri S G/Asha T	IA	Date	
Social Connect and Responsibility	21UH36	Suprith P G/ Deepika V B	I		
Samskrutika/Balake Kannada	21KSK37/21KBK37	Deepa B S	II		
LIC Lab Using MultiSIM	21EC383	Harisha G C/Sampath Kumar B	III		
NSS/PE/YOGA	21NS/PE/YO				

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

CLASS TIME TABLE

W.E.F: 10-10-2022

Academic Year		Scheme	Semester	Section	Room No	Class Coordinator	Semester Commencement		Last Working Day	
2022-23		2021	V	A	329	Dr. Rajashekhar S	10-10-2022		27-1-2023	
Time	8.30 am To 9.30 am	9.30 am To 10.30 am	10.30 am To 10.50 am	10.50 am To 11.50 am	11.50 am To 12.50 pm	12.50 pm To 1.45 pm	1.45 pm To 2.40 pm	2.40 pm To 3.35 pm	3.35 pm To 4.30 pm	
MON	EMW	←----- DSP(A1)/HDL(A2) -----→				LUNCH BREAK	DSP	VHDL	EVS	
TUE	TIME	DSP	TEA BREAK	PCS	VHDL		←----- DSP(A2)/HDL(A3) -----→			
WED	DSP	ITC		VHDL	EMW		DSP	ITC	TIME	
THU	PCS	←----- DSP(A3)/HDL(A1) -----→						←----- Forum Activity -----→		
FRI	VHDL	ITC	TEA BREAK	TIME	DSP		ITC	EMW	PCS	
SAT	ITC	PCS		EMW	TIME					
Allocation of Subjects							Faculty Mentors			
Subject Name		Subject Code	Course Coordinator		Batch	Faculty Name				
Technological Innovation Mgmt & Entr		18ES51	Suprith P G		A1	Rajendra Soloni				
Digital Signal Processing		18EC52	Dr. Rajashekhar Somasagar		A2	Raviteja B				
Principle of Communication System		18EC53	Manjula B K		A3	Chaitra S N				
Information Theory Coding		18EC54	Harisha G C			Internal Assessment				
Electromagnetic Waves		18EC55	Rajendra Soloni			IA	Date			
Verilog HDL		18EC56	Chetan B V			I				
DSP Lab		18ECL57	Dr. Rajashekhar S/Dr. Kavitha K J/ RSS/AT			II				
HDL Lab		18ECL58	Chetan B V/SGR/SPG			III				
Environmental Studies		18CIV59	Virupaksha H V							

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W.E.F: 10-10-2022

Academic Year		Scheme	Semester	Section	Room No	Class Coordinator	Semester Commencement		Last Working Day
2022-23		2021	V	B	333	Suprith P G	10-10-2022		27-1-2023
Time	8.30 am To 9.30 am	9.30 am To 10.30 am	10.30 am To 10.50 am	10.50 am To 11.50 am	11.50 am To 12.50 pm	12.50 pm To 1.45 pm	1.45 pm To 2.40 pm	2.40 pm To 3.35 pm	3.35 pm To 4.30 pm
Day	MON	EMW	TIME	VHDL	ITC	LUNCH BREAK	←----- DSP(B1)/HDL(B2) -----→		
	TUE	ITC	←----- DSP(B2)/HDL(B3) -----→				PCS	VHDL	ITC
	WED	PCS	TIME	DSP	EMW		←----- DSP(B3)/HDL(B1) -----→		
	THU	TIME	VHDL	DSP	ITC		←----- Forum Activity -----→		
	FRI	ITC	DSP	PCS	EMW		TIME	DSP	EVS
	SAT	DSP	EMW	VHDL	PCS				
			TEA BREAK						
Allocation of Subjects							Faculty Mentors		
Subject Name		Subject Code	Course Coordinator		Batch	Faculty Name			
Technological Innovation Mgmt & Entr		18ES51	Suprith P G		B1	Manjula B K			
Digital Signal Processing		18EC52	Dr. Rajashekar Somasagar		B2	Dr. Rajashekar Somasagar			
Principle of Communication System		18EC53	Manjula B K		B3	Dr. Kavitha K J			
Information Theory Coding		18EC54	Deepika V B		Internal Assessment				
Electromagnetic Waves		18EC55	Raviteja B		IA	Date			
Verilog HDL		18EC56	Dr. Praveen J		I				
DSP Lab		18ECL57	Rajendra Soloni/ Akshata Chavan		II				
HDL Lab		18ECL58	Hanumanta Raju R K/SPG/CBV		III				
Environmental Studies		18CIV59	Dr. Arunkumar S L						

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

CLASS TIME TABLE

W.E.F: 19/09/2022

Academic Year		Scheme	Semester	Section	Room No	Class Coordinator	Semester Commencement		Last Working Day
2022-23		2018	VII	A	342	Akshata C	19/09/2022		31/12/2022
Time	8.30 am To 9.30 am	9.30 am To 10.30 am	10.30 am To 10.50 am	10.50 am To 11.50 am	11.50 am To 12.50 pm	12.50 pm To 1.45 pm	1.45 pm To 2.40 pm	2.40 pm To 3.35 pm	3.35 pm To 4.30 pm
Day	MON	VLSI	CN	TEA BREAK	RTS	AE	LUNCH BREAK		
	TUE	OE	OE	TEA BREAK	AE	VLSI			
	WED	SEMINAR	<----- VLSI Lab A1 / CN Lab A2 ----->						
	THU	CN	RTS	TEA BREAK	OE	OE			
	FRI	RTS	AE	TEA BREAK	VLSI	CN			
	SAT	AE	VLSI	TEA BREAK	CN	RTS			
Allocation of Subjects						Faculty Mentors			
Subject Name		Subject Code	Course Coordinator		Batch	Faculty Name			
Computer Networks		18EC71	Chaitra S N		A1	Sampath Kumar B			
VLSI Design		18EC72	Deepika V B		A2	Vikas C Yatnalli			
Real Time Systems		18EC731	Deepa T		A3	Harisha G C			
Automotive Electronics		18EC742	Akshata C		Internal Assessment				
Computer Networks Lab		18ECL76	Deepa T						
VLSI Lab		18ECL77	Deepika V B		I	Date			
Project Work Phase-1		18ECP78	Dr. Rajashekar S		II				
Open Elective* - Offered by other departments					III				

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Academic Year		Scheme	Semester	Section	Room No	Class Coordinator	Semester Commencement		Last Working Day
2022-23		2018	VII	B	335	Chaitra S N	19/09/2022		31/12/2022
Time	8.30 am To 9.30 am	9.30 am To 10.30 am	10.30 am To 10.50 am	10.50 am To 11.50 am	11.50 am To 12.50 pm	12.50 pm To 1.45 pm	1.45 pm To 2.40 pm	2.40 pm To 3.35 pm	3.35 pm To 4.30 pm
Day	MON	RTS	AE	TEA BREAK	VLSI	CN	<----- Project Work ----->		
	TUE	OE	OE		RTS	VLSI	CN	AE	RTS
	WED	VLSI	RTS		AE	CN	<----- Project Work ----->		
	THU	AE	VLSI		OE	OE	<----- Forum Activity ----->		
	FRI	SEMINAR	<----- VLSI Lab B1 / CN Lab B2 ----->			<----- Project Work ----->			
	SAT	CN	<----- VLSI Lab B2 / CN Lab B1 ----->						
Allocation of Subjects						Faculty Mentors			
Subject Name		Subject Code	Course Coordinator		Batch	Faculty Name			
Computer Networks		18EC71	Chaitra S N		B1	Chetan B V			
VLSI Design		18EC72	Santosh B G		B2	Akshata C			
Real Time Systems		18EC731	Deepa T		B3	Suprith P G			
Automotive Electronics		18EC742	Akshata C		Internal Assessment				
Computer Networks Lab		18ECL76	Chaitra S N		IA	Date			
VLSI Lab		18ECL77	Santosh B G		I				
Project Work Phase-1		18ECP78	Rajendra Soloni		II				
Open Elective* - Offered by other departments					III				

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